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THIRD EDITION Computer Organization and Design THE HARDWARE/SOFTWARE INTERFACE David A. Patterson University of California, Berkeley John L. Hennessy Stanford University With a contribution by Peter J. Ashenden James R. Larus Daniel J. Sorin Ashenden Designs Pty Ltd Microsoft Research Duke University AMSTERDAM • BOSTON • HEIDELBERG • LONDON

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single clock design are mapped down to the gate level. With modern computer-aided design (CAD) systems, this process is completely mechanical. The examples illustrate how a CAD system takes advantage of the structure of the control function, including the presence of don't-care terms. Mapping the ALU Control Function to Gates

Appendix D Mapping Control to Hardware

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