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Professor Lee's principal areas of professional interest include analog circuitry of all types, ranging from low-level DC instrumentation to high-speed RF communications systems. His present research focus is on CMOS RF integrated circuit design, and on extending operation into the terahertz realm.

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TEXTBOOK B. Razavi, RF Microelectronics (2nd Edition), Pearson, 09/ 2011 REFERENCE: Thomas H. Lee, The Design of CMOS Radio-Frequency Integrated Circuits, Cambridge University Press, 2nd Ed., 2004 Robert Caverly, CMOS RFIC Design Principles, Artech House, 2007 SOFTWARE: Cadence IC Design Tools GOALS: 1.

ECE 413/513 - Radio-Frequency IC Design

Thomas H. Lee is a professor in the Department of Electrical Engineering at Stanford University. Lee's research focus has been on gigahertz-speed wireline and wireless integrated circuits built in conventional silicon technologies, particularly CMOS; microwave; and RF circuits.. Things about Stuff is a popular freshman course, taught by Lee. This course tells stories behind the greatest ...

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